

**Notic of References Cited**

Application/Control No.

09/691,083

Applicant(s)/Patent Under  
Reexamination  
BODEN ET AL.

Examiner

Brook Kebede

Art Unit

2823

Page 1 of 1

**U.S. PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Name                 | Classification |
|---|---|--|-----------------|----------------------|----------------|
|   | A | US-5,248,627                                     | 09-1993         | Williams, Richard K. | 438/268        |
|   | B | US-5,418,174                                     | 05-1995         | Kalnitsky, Alexander | 438/384        |
|   | C | US-  |                 |                      |                |
|   | D | US-  |                 |                      |                |
|   | E | US-  |                 |                      |                |
|   | F | US-  |                 |                      |                |
|   | G | US-  |                 |                      |                |
|   | H | US-  |                 |                      |                |
|   | I | US-  |                 |                      |                |
|   | J | US-  |                 |                      |                |
|   | K | US-  |                 |                      |                |
|   | L | US-  |                 |                      |                |
|   | M | US-  |                 |                      |                |

**FOREIGN PATENT DOCUMENTS**

| * |   | Document Number<br>Country Code-Number-Kind Code | Date<br>MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
|   | N |  |                 |         |      |                |
|   | O |  |                 |         |      |                |
|   | P |  |                 |         |      |                |
|   | Q |  |                 |         |      |                |
|   | R |  |                 |         |      |                |
|   | S |  |                 |         |      |                |
|   | T |  |                 |         |      |                |

**NON-PATENT DOCUMENTS**

| * |   | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)            |
|---|---|--|
|   | U | Wolf et al., "Silicon Processing for the VLSI Era, Process Integration" Volume 2, PP. 194-196, 1990. |
|   | V |  |
|   | W |  |
|   | X |  |

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.